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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
CROCE ET AL.

Serial No. **09/839,596**

Confirmation No. **1496**

Filing Date: **April 20, 2001**

For: **RESURF LDMOS INTEGRATED
STRUCTURE**

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) Examiner Ori Nadav
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) Art Unit: 2811
)
) Attorney Docket:
) 99AG39853288
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TECHNOLOGY CENTER 2800

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RESPONSE TO REQUIREMENT FOR RESTRICTION

Director, U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

In response to the restriction requirement of May 17, 2002 Applicant hereby elects Group I, Claims 5-18, drawn to a semiconductor device in Class 257, subclass 335. It is respectfully requested that the Examiner use his discretion under 35 U.S.C. §121 and withdraw the restriction requirement since search and examination of the entire application can be made without serious burden.

If any extension and/or fee is required, authorization is given to charge Deposit Account No. **01-0484**.

Respectfully submitted,

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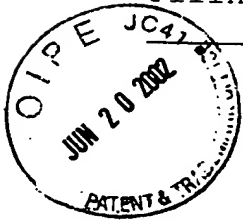
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CROCE ET AL.

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE, WASHINGTON, D.C. 20231, on this 14th day of June, 2002.

Kevin L. Peterson

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